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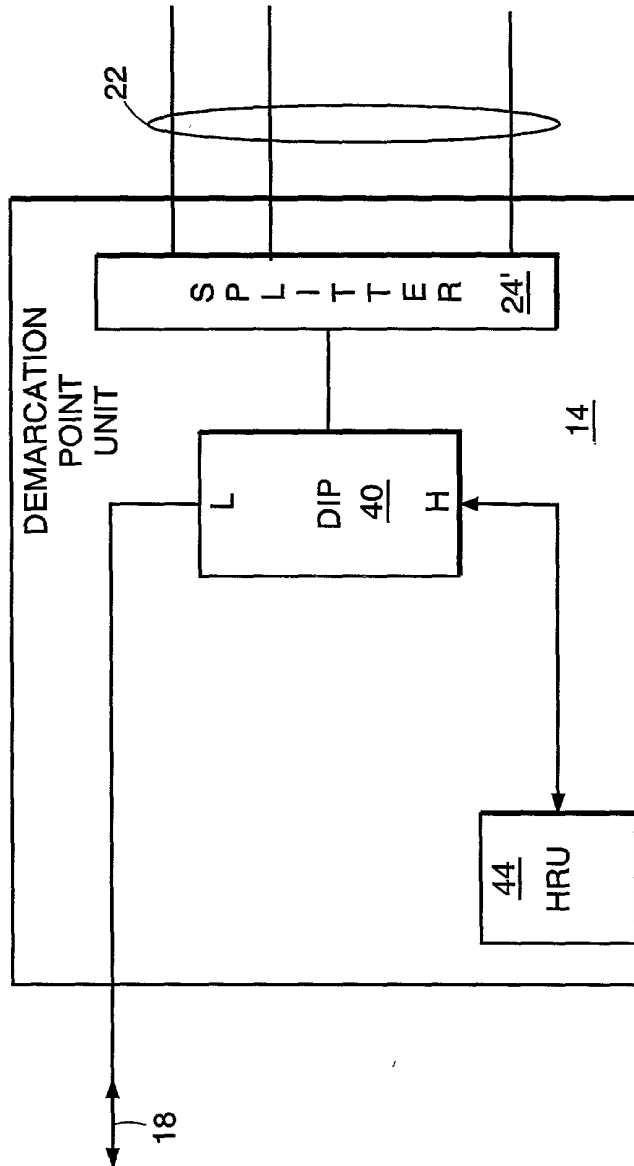


FIG. 2

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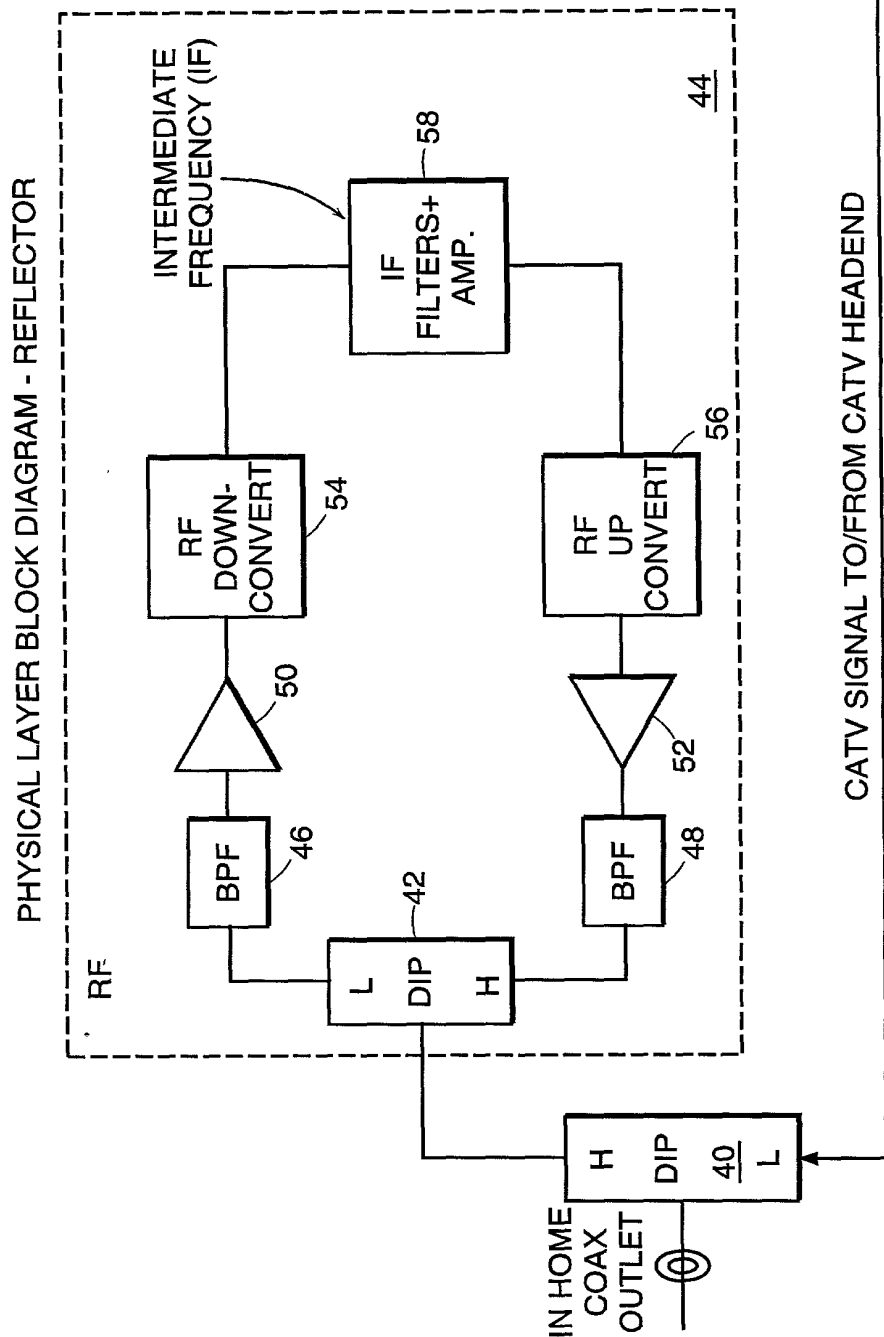


FIG. 2A

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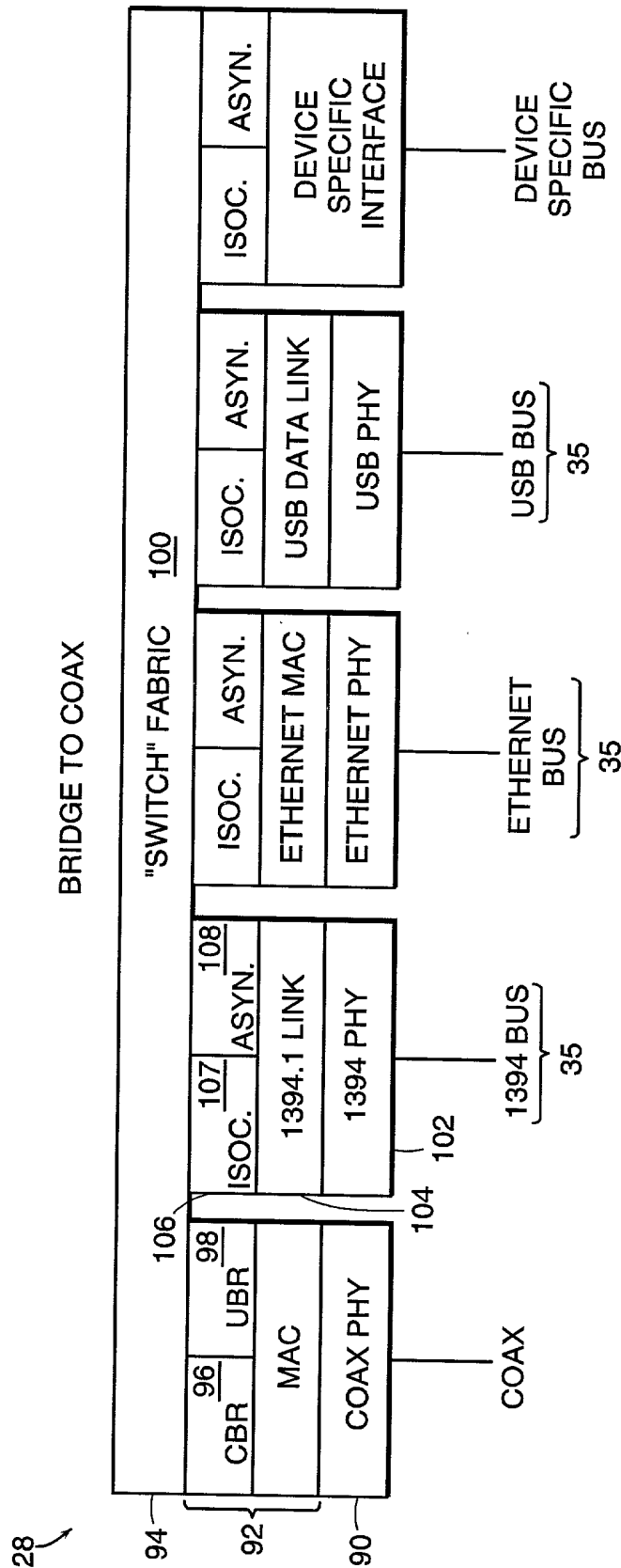


FIG. 3

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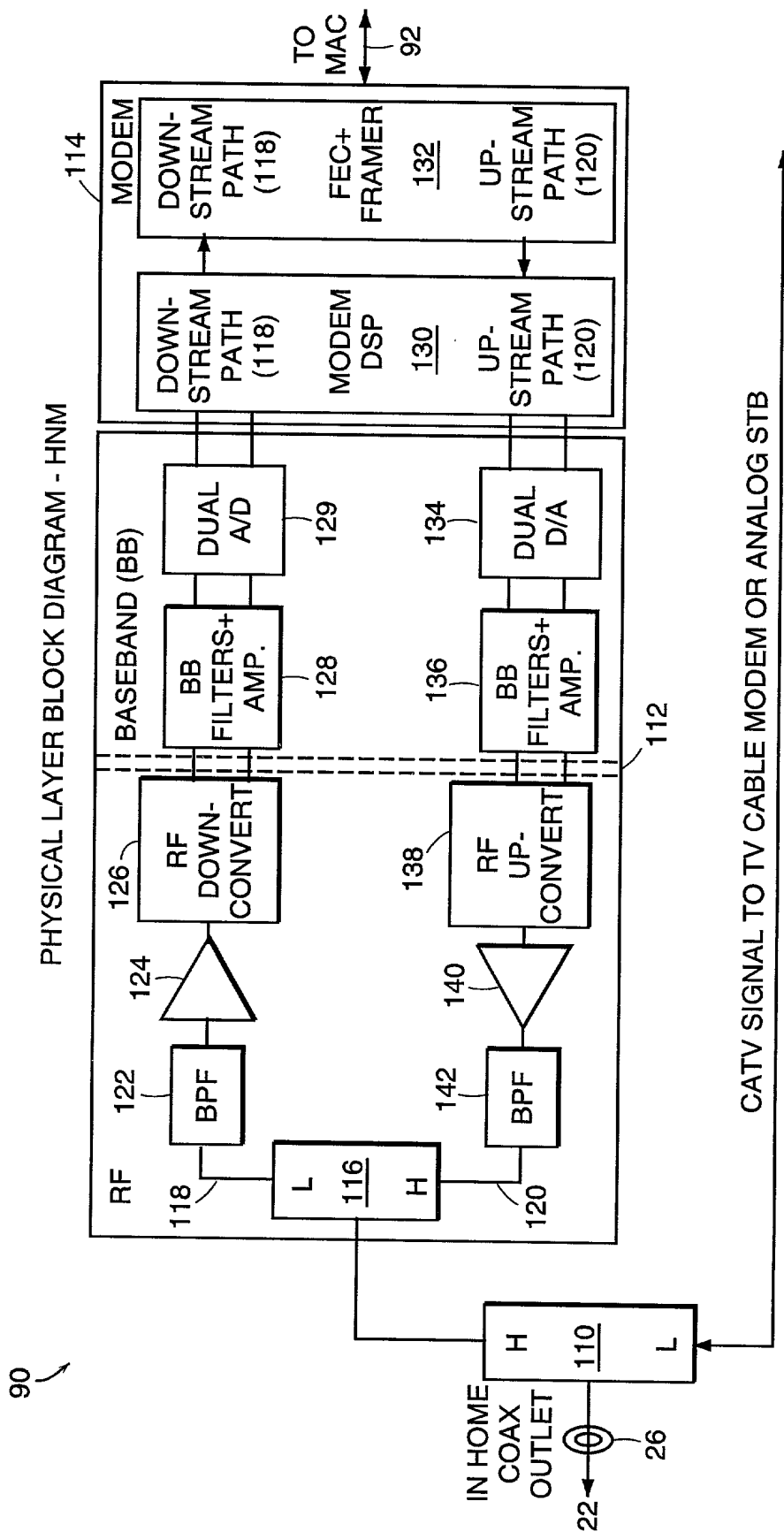


FIG. 4



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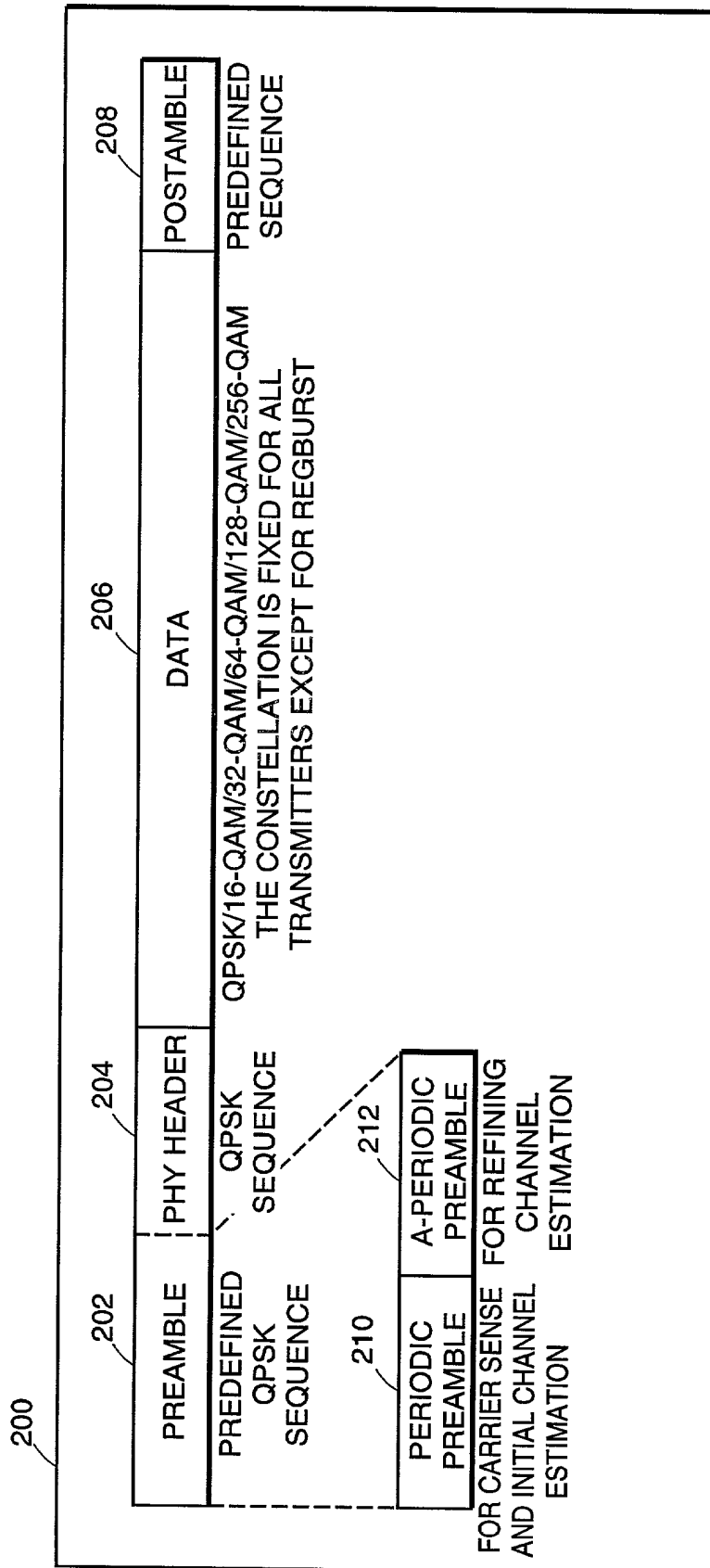


FIG. 6

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FIG. 7A
FIG. 7B
FIG. 7C

FIG. 7

(A) EACH TRANSMISSION CYCLE STARTS WITH A MASTER CBR BURST -CYCLE START BURST

(B) AFTER ACK CYCLE, THE FOLLOWING MASTER UBR BURST (CONSIDERING THE CBR, UBR PRIORITY) MARKS THE START OF THE FAIRNESS CYCLE WITH FAIRN_START_BURST.

(C) AFTER REGISTRATION PERIOD, THE FOLLOWING MASTER BURST STARTS THE ACK CYCLE WITH ITS ACK BURST.

FIG. 7A

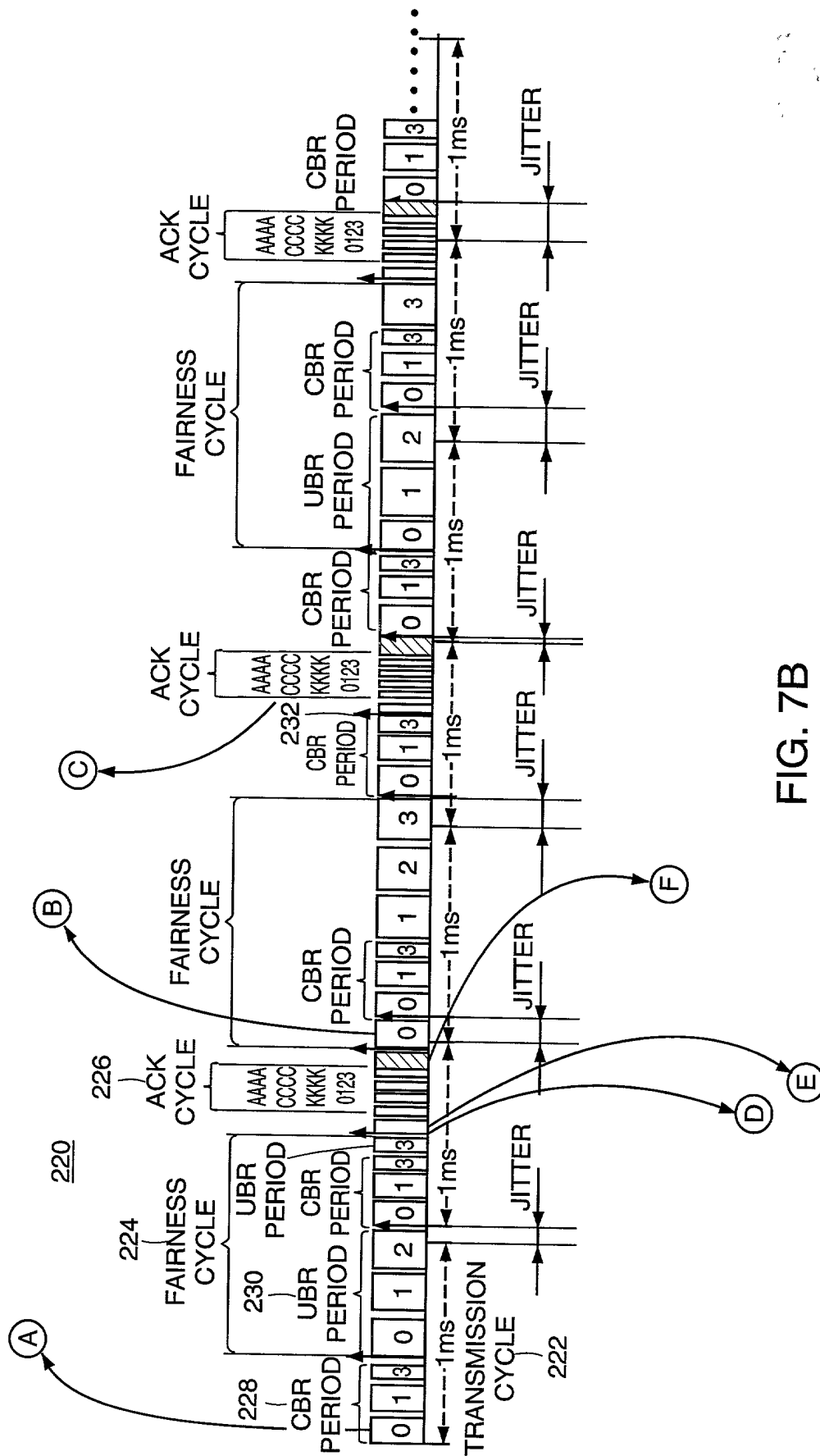


FIG. 7B

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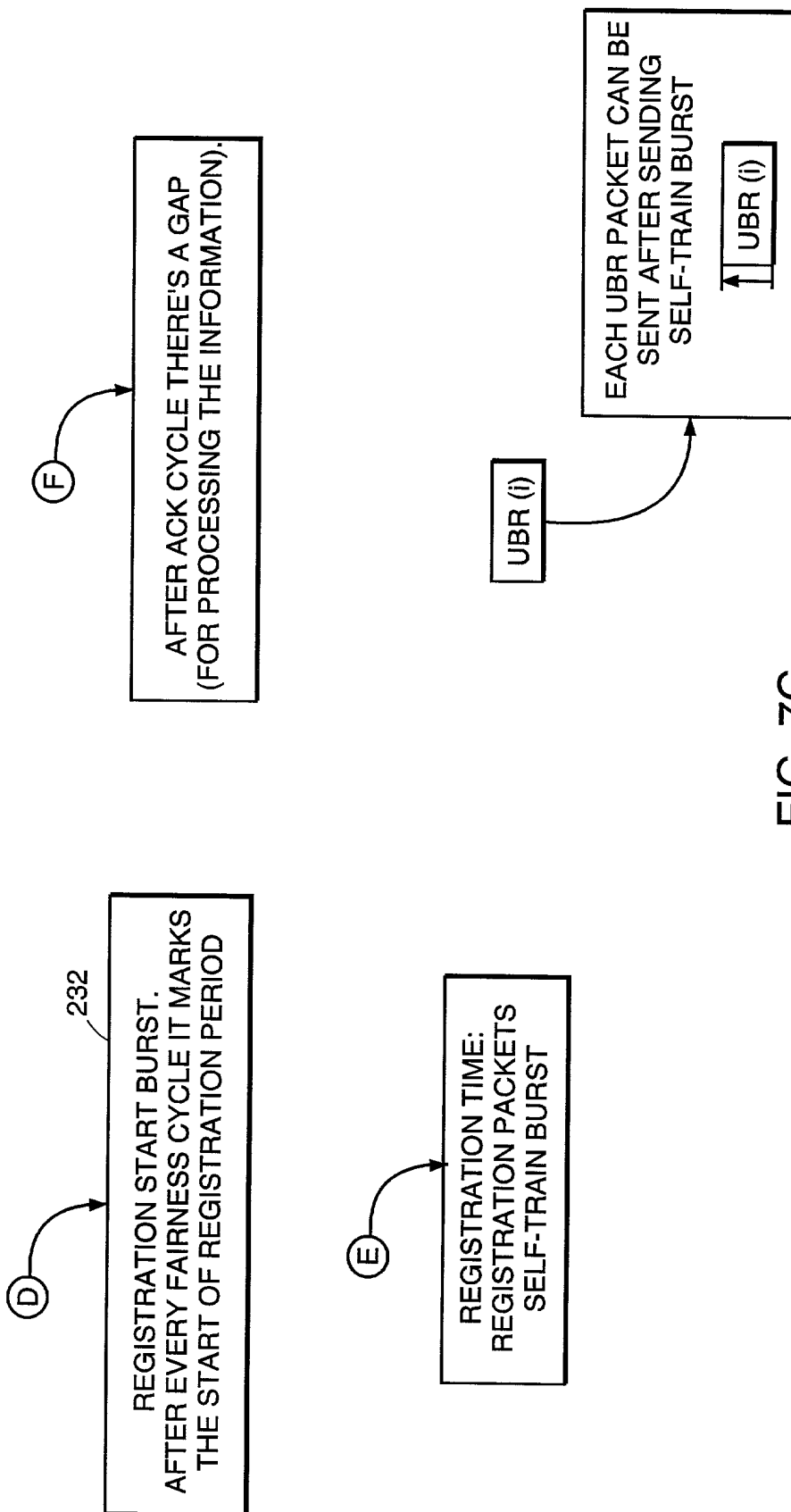


FIG. 7C

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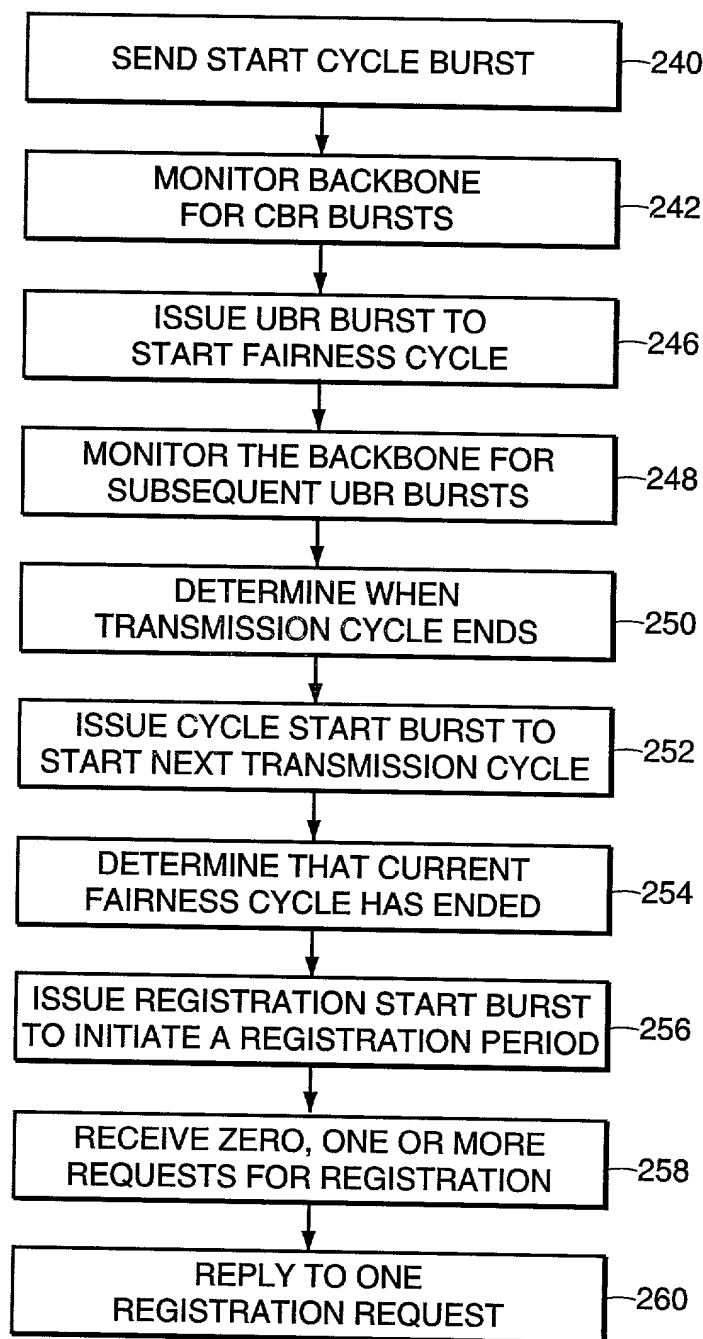


FIG. 8A

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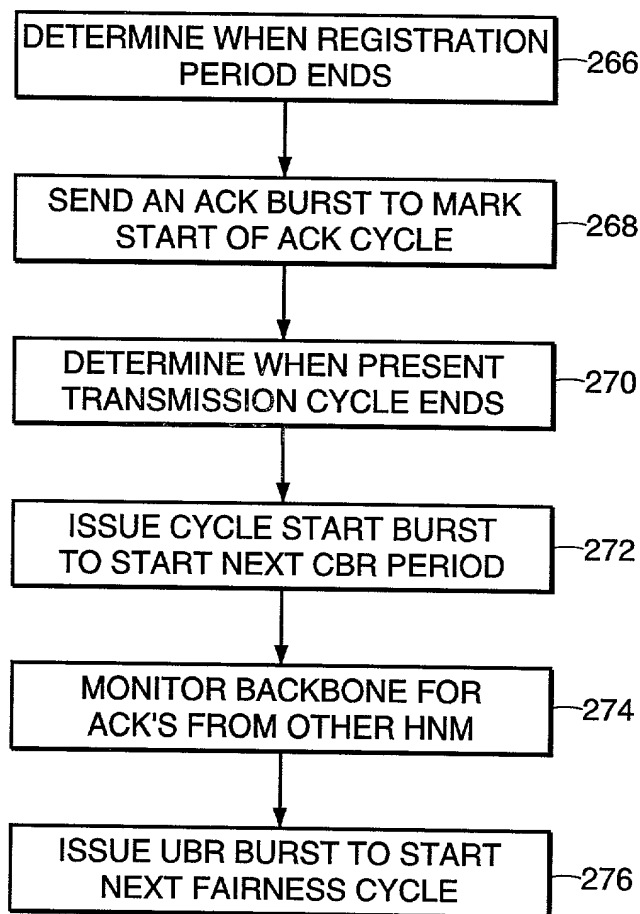


FIG. 8B

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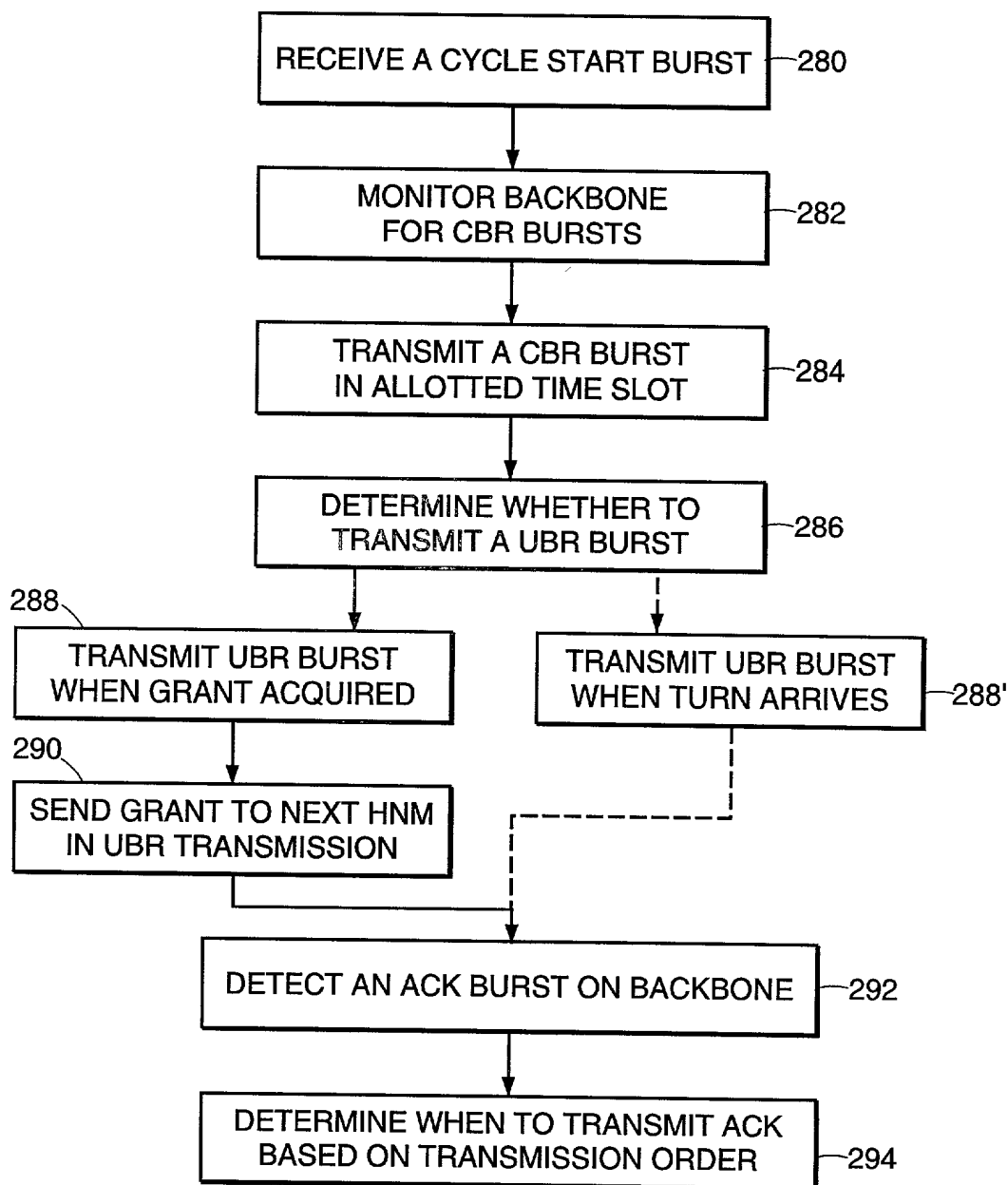


FIG. 9

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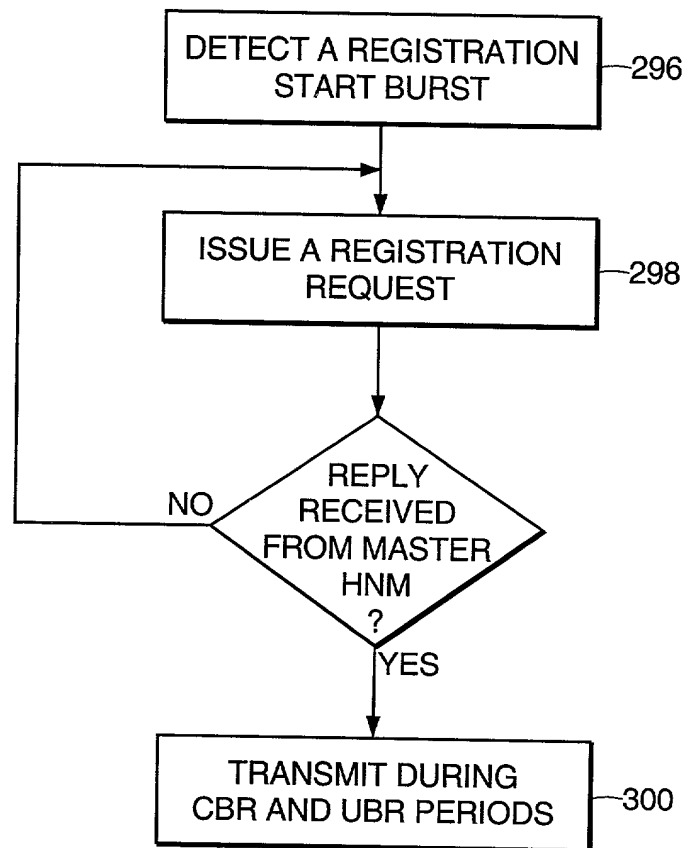


FIG. 10

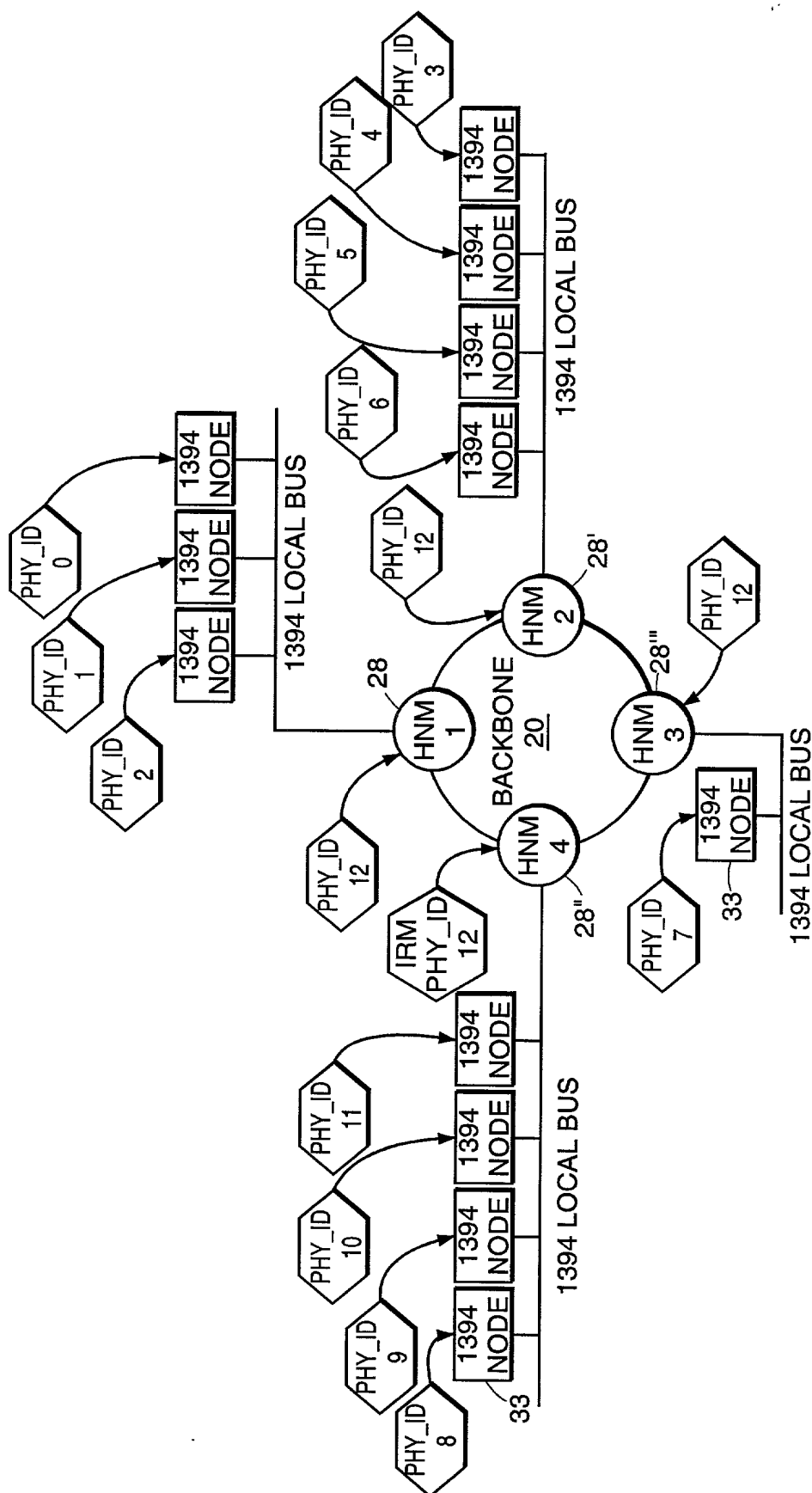


FIG. 11

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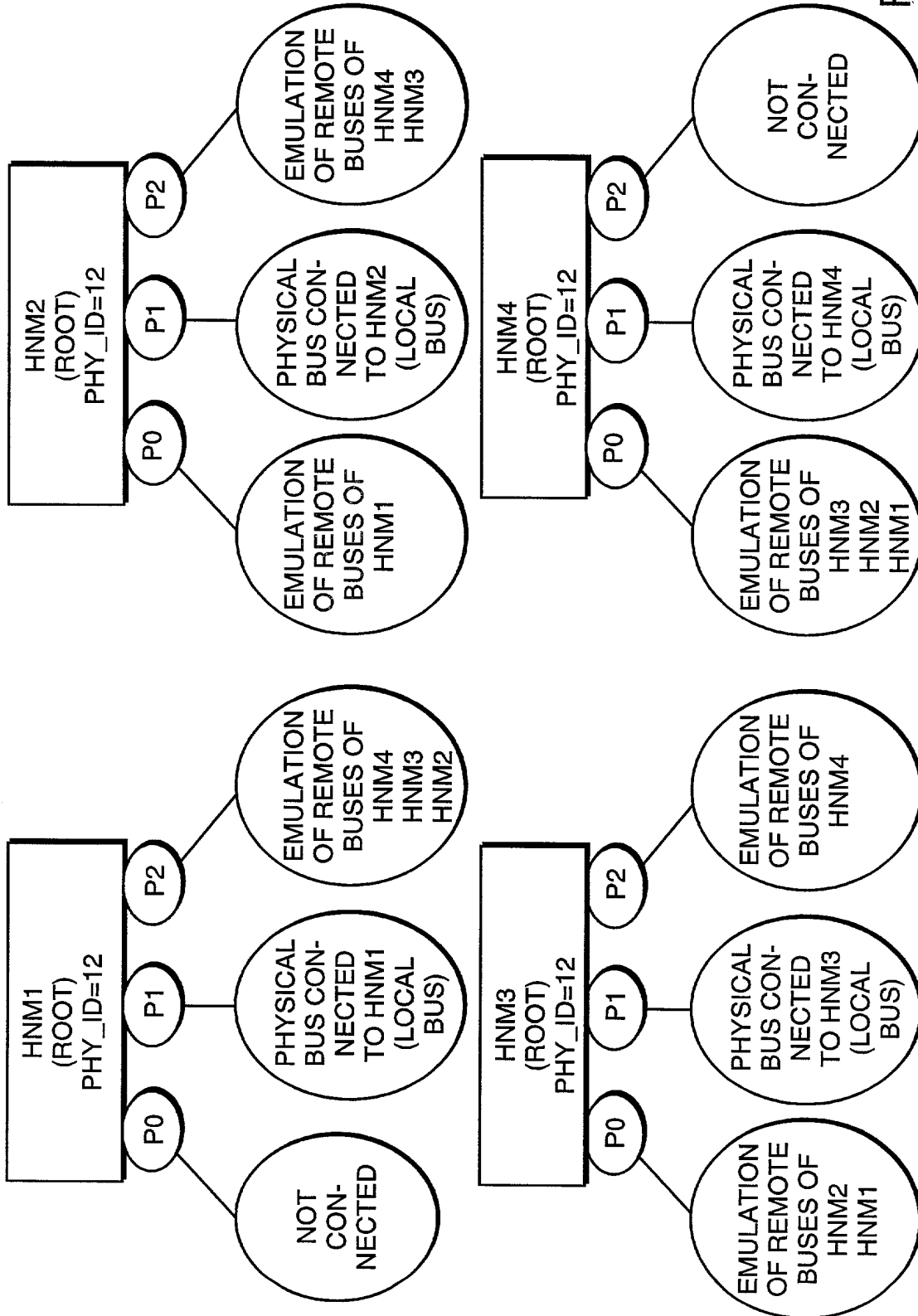


FIG. 12

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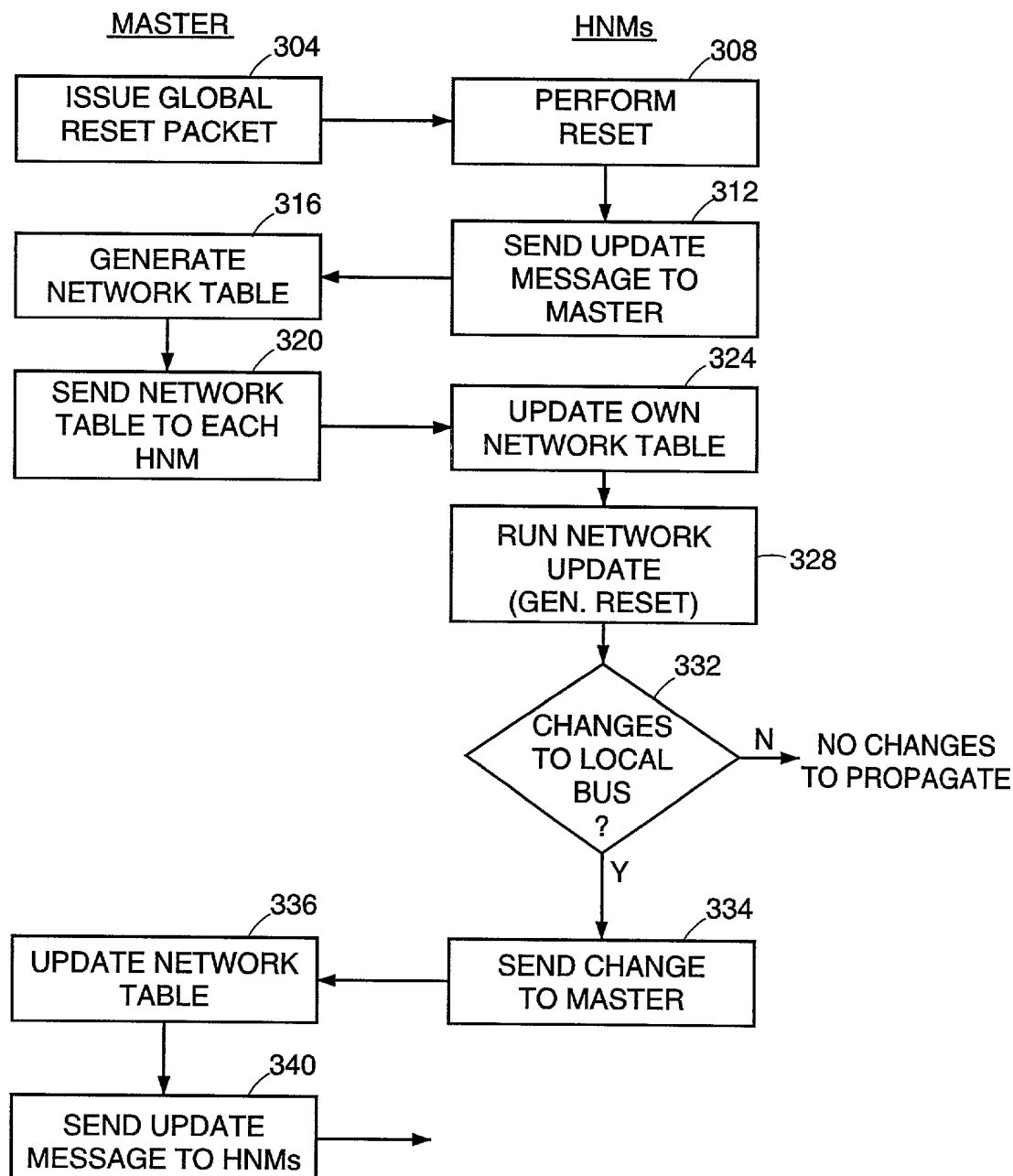


FIG. 13

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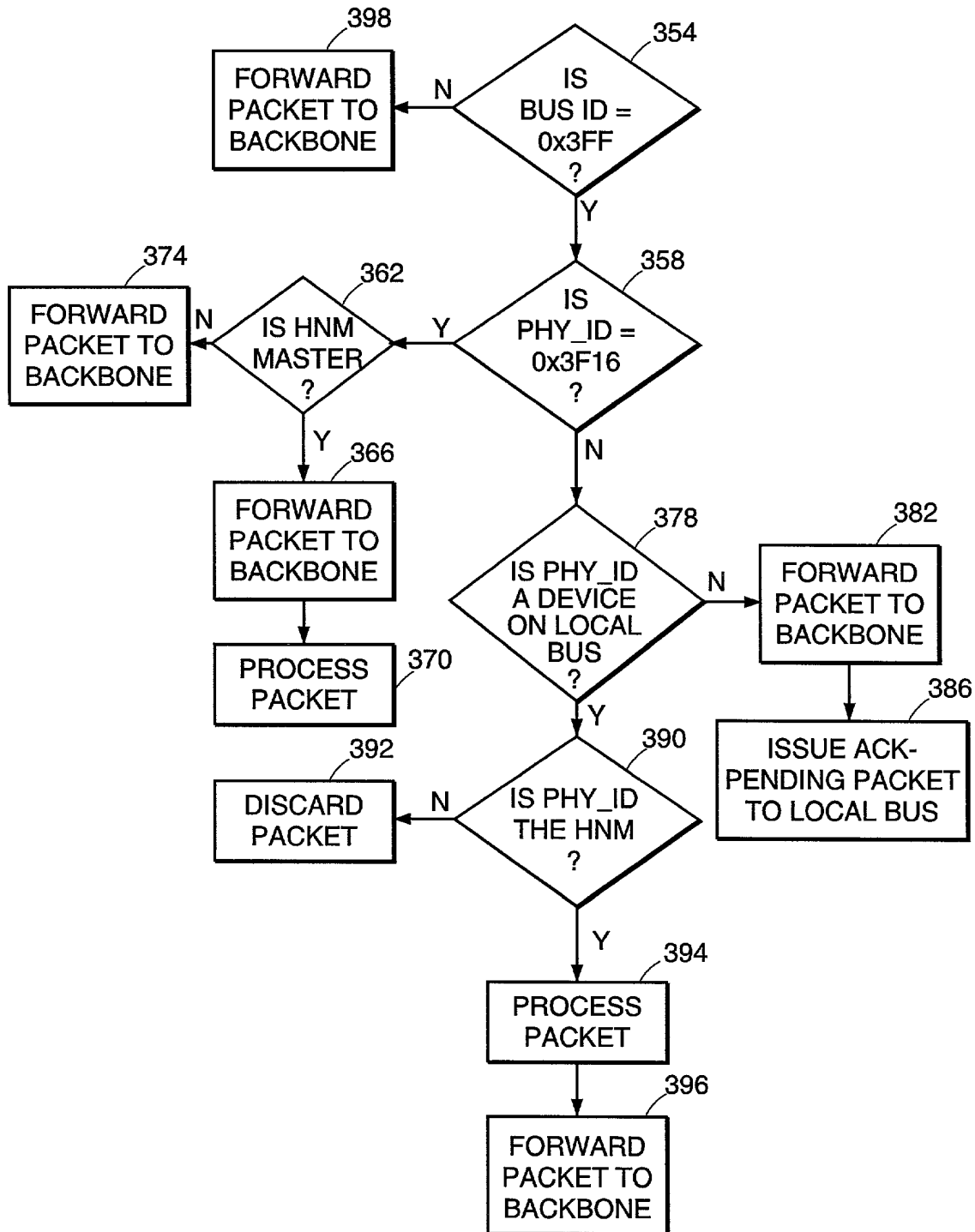


FIG. 14

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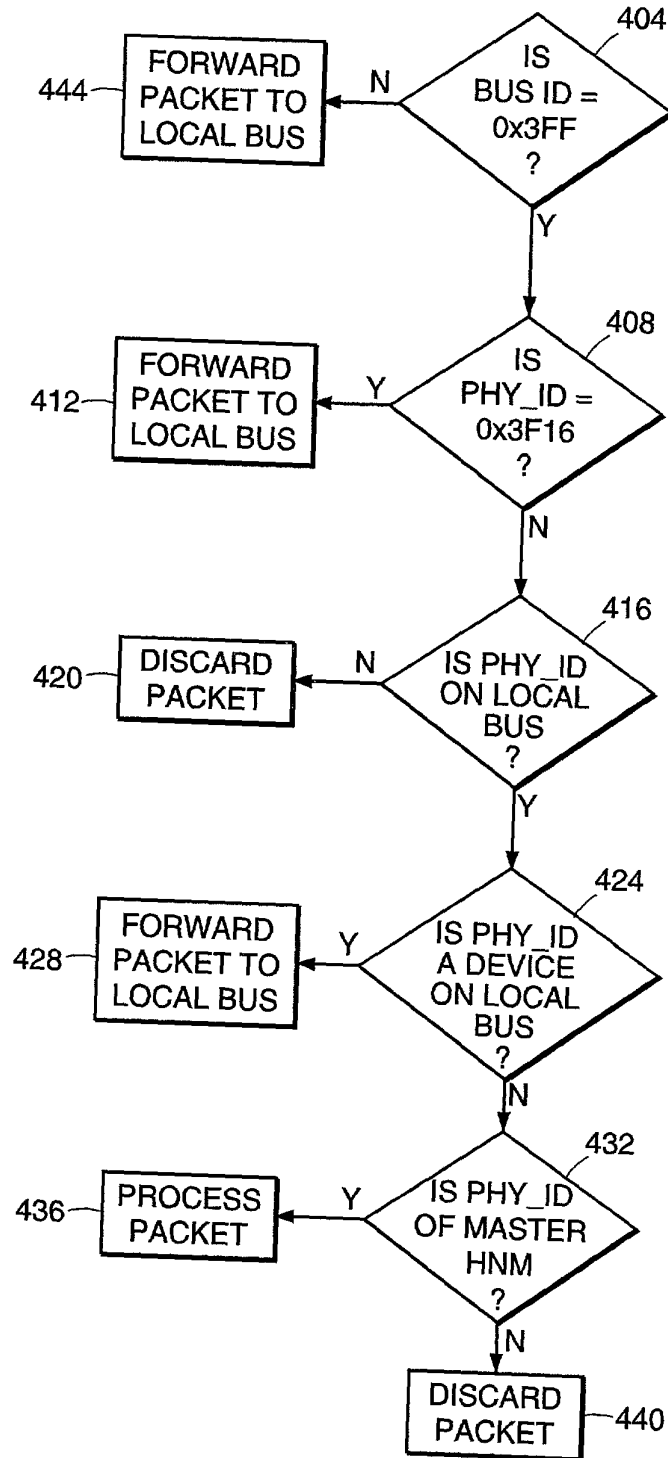


FIG. 15

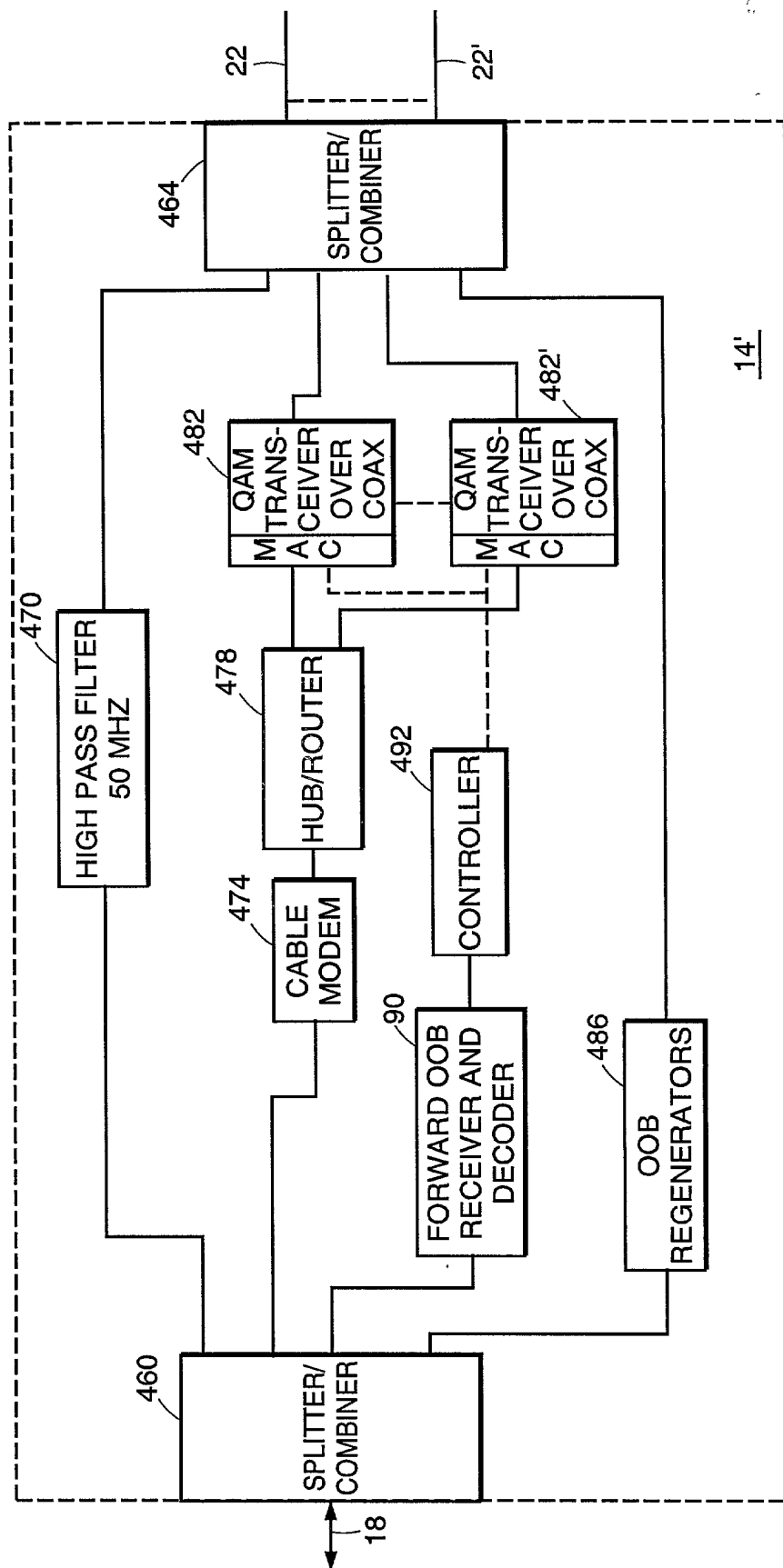


FIG. 16